

Application Number:	10/672,961
Filing or 371 (c) Date:	09-26-2003
Application Type:	Utility
Examiner Name:	DICKEY, THOMAS
Group Art Unit:	2826
Confirmation Number:	9439
Attorney Docket Number:	ELM-2 CONT. 4
Class / Subclass:	438/238
First Named Inventor:	Glenn Leedy , Saline, MI

Title of Invention: Three dimensional multi layer memory and control logic  
integrated circuit structure

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL RESPONSE**

Sir:

Supplemental to the prior response of 12/21/2009, please amend this application  
as follows.